

PERFORMANCE OF TURBO CODED SIGNALS OVER FADING CHANNELS

TURBO KODLANMIŞ İPARETLERİN SÖNÜMLEMELİ KANALLARDA BAŞARIM ANALİZİ

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ABSTRACT

In this paper, the performance of turbo coded signals are investigated for different frame numbers and encoder structures over Rician fading channels. The numerical results demonstrate the error performance degradation due to fading channel frame numbers and the state of the encoder.

Key Words: Turbo Coding, Rician Fading Channel

ÖZET

Bu makale de, farklı çerçeve uzunlukları, kodlayıcı özelliklerine göre değerlendirilerek turbo kodlanmış iparetlerin Rician ortamlarda başarımlı incelenmiştir. Benzetim sonuçları, söz konusu Rician ortamda farklı parametreler için değerlendirilmiştir.

Anahtar Kelimeler: Turbo kodlama, Rician Sönümleme Kanalı

I. INTRODUCTION

Turbo codes are a new class of error correction codes that were introduced a long with a practical decoding algorithm in [1]. The importance of turbo codes is that they enable reliable communications with power efficiencies close to the theoretical limit predicted by Claude Shannon [2]. Since their introduction, turbo codes have been proposed for low-power applications such as deep-space and satellite

communications, as well as for interference limited applications such as third generation cellular and personal communication services.

The main principle in the turbo coding scheme is to use two codes in parallel. This means that the information sequence is encoded twice, the second time after a scrambling of the information bits. The component codes are chosen as small convolutional codes in the recursive systematic form. With this encoding we are able to decode

the two encoded streams with an iterative process using two soft-in soft-out decoders, one corresponding to each of the encoders. For the simulations shown in this paper we have used a MAP decoder. The decoders exchange information as a priori probabilities for the information bits.

II. SYSTEM MODEL

The block diagram of the channel model that we use in this paper is shown in Fig. 1. The channel output u_k is

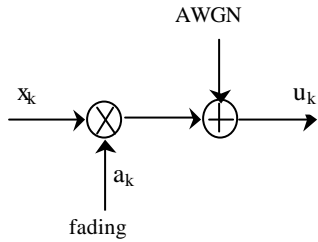


Figure 1. Channel Model

$$u_k = a_k \cdot x_k + n_k \tag{1}$$

n_k is Gaussian Noise where the noise variance $\sigma^2 = N_0/2E_s$, a_k is fading amplitude for the signaling interval k .

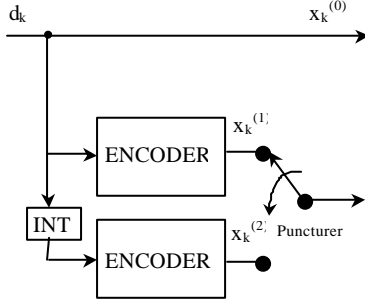


Figure 2. Turbo Encoder for $1/2 D^n / PRFC$ Model

In the encoder structures we use Recursive Systematic Convolutional (RSC) encoders as Turbo encoders. Consider a half-rate RSC encoder with M memory size. If the d_k is an input at time k the output X_k is equal to

$$X_k = d_k \tag{2}$$

Remainder $r(D)$ can be found using feedback polynomial $g^{(0)}(D)$ and feedforward polynomial is $g^{(1)}(D)$. The feedback variable is

$$r_i = d_k + \sum_{j=1}^M r_{k-j} g_j^{(0)} \tag{3}$$

and RSC encoder output Y_k which called parity data [7], is

$$Y_k = \sum_{j=0}^M r_{k-j} g_j^{(1)} \tag{4}$$

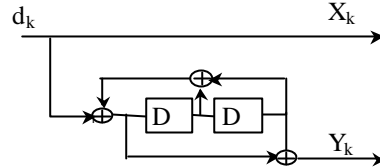


Figure 3. Recursive Systematic Convolutional (RSC) Encoder

RSC encoder with memory $M=2$ and rate $R=1/2$ which feedback polynomial $g^{(0)}=7$ and feedforward polynomial $g^{(1)}=5$ thus $g^{(1)}[111;101]$ is illustrated in Fig. 3 and it has a generator matrix

$$G(D) = \begin{bmatrix} 1 & 1 + D + D^2 \\ 1 & 1 + D^2 \end{bmatrix} \tag{5}$$

III. TURBO DECODING

The problem of estimating the state sequence of a Markov process observed through noise has two well known trellis-based solutions- Viterbi Algorithm (VA) [8] and the symbol-by-symbol maximum a posteriori (MAP) algorithm. The key difference between algorithms is that the states estimated by the VA must form a connected path through the trellis, while the states estimated by the MAP algorithm need not to be connected. When applied to digital transmission systems, the VA minimizes the frame error rate (FER), while the MAP algorithm minimizes the bit error rate (BER)

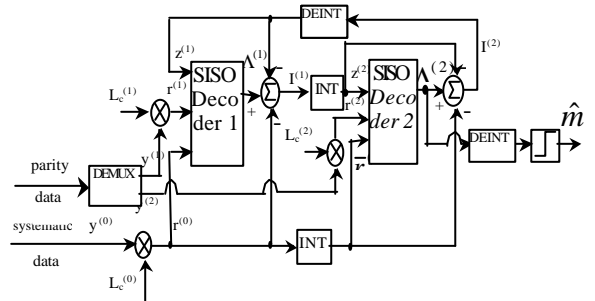


Figure 4. Turbo Decoder

The problem of decoding turbo codes involves the joint estimation of two Markov processes, one for each constituent code. While in theory it

is possible to model a turbo code as a single Markov process, such a representation is extremely complex and does not lend itself to computationally tractable decoding algorithms. Turbo decoding proceeds instead by first independently estimating the individual Markov processes. Because the two Markov processes are defined by the same set of data, the estimates can be refined by sharing information between the two decoders in an iterative fashion. More specifically, the output of one decoder can be used as a priori information by the other decoder (Fig. 4). If the outputs of the individual decoders are in the form of hard-bit decisions, then there is little advantage to sharing information. However, if soft-bit decisions are produced by the individual decoders, considerable performance gains can be achieved by executing multiple iterations of decoding [6].

A. Soft-Input, Soft Output (SISO) Decoding

In the MAP decoder, the output is given by

$$\Lambda_k = \ln \frac{P[m_k = 1 | \mathbf{y}]}{P[m_k = 0 | \mathbf{y}]} \quad (6)$$

m is message bit and \mathbf{y} is received sequence. There are three inputs for Soft-Input Soft-Output (SISO) decoder. $y_k^{(s)}$ is the systematic observation, $y_k^{(p)}$ is the parity information and z_k is the priori information which is derived from the other decoder's output. The log-likelihood at the output of a SISO decoder using the channel model can be factored into three terms as [6]

$$\Lambda_k = \frac{4a_k^{(s)} \cos(\mathbf{q}_k^{(s)}) E_s}{N_0} y_k^{(s)} + z_k + l_k \quad (7)$$

where the term l_k is called the extrinsic information, a_k is fading amplitude, E_s is the energy per code symbol and N_0 is the noise power. The priori information at the input of one decoder is found by subtracting two values from its output to prevent "positive feedback problem" as shown in Fig. 4 of M.C. Valenti study [6]. First of all we make the following notation;

$$L_c = \frac{4a_k \cos(\mathbf{q}_k) E_s}{N_0} \quad (8)$$

which is called the reliability of the channel, and

$$l_k = \Lambda_k - L_c^{(s)} y_k^{(s)} - z_k \quad (9)$$

The MAP algorithm attempts to find the most likely individual state s_k given \mathbf{y}

$$\hat{s}_k = \arg \left\{ \max_{s_k} P[s_k | \mathbf{y}] \right\} \quad (10)$$

Before finding the a posteriori probabilities (APPs) for the message bits, the MAP algorithm finds the

probability of $P[s_k \rightarrow s_{k+1} | \mathbf{y}]$ of each valid state transition given the noisy channel observation \mathbf{y} .

$$P[s_k \rightarrow s_{k+1} | \mathbf{y}] = \frac{P[s_k \rightarrow s_{k+1}, \mathbf{y}]}{P[\mathbf{y}]} \quad (11)$$

B. Log-MAP Algorithm

The maximum a posteriori (MAP) algorithm can calculate the a posteriori probability of each bit with a perfect performance. However there are known problems as the number of calculation depending on the memory states. These problems can be solved by performing the entire algorithm in the log domain [9][10]. To illustrate how performed in the log domain, consider the *Jacobian Logarithm*.

$$\ln(e^x + e^y) = \max(x, y) + \ln(1 + \exp\{-|y-x|\}) \quad (12)$$

$$= \max(x, y) + f_c(|x-y|)$$

this equation describes the log-MAP algorithm with a correction function f_c .

Let $\bar{\mathbf{g}}(s_k \rightarrow s_{k+1})$ denoted the natural logarithm of

$$\bar{\mathbf{g}}(s_k \rightarrow s_{k+1}) = \ln \mathbf{g}(s_k \rightarrow s_{k+1}) \quad (13)$$

$$= \ln P[m_k] + \ln P[y_k | x_k] \quad (14)$$

$$\ln P[m_k] = z_k m_k - \ln(1 + e^{z_k}) \quad (15)$$

and (14) becomes

$$\bar{\mathbf{g}}(s_k \rightarrow s_{k+1}) = \ln P[m_k] - \frac{1}{2} \ln(\mathbf{P} N_0 / E_s)$$

$$- \frac{E_s}{N_0} \sum_{q=0}^{n-1} \left[y_k^{(q)} - (-abs(a_k^{(q)} x_k^{(q)} \cos(\mathbf{q}_k^{(q)} + 1)) \right]^2$$

$$= \mathbf{I}(s_k \rightarrow s_{k+1}) \quad (16)$$

Now let $\bar{\mathbf{a}}(s_k)$ be the natural logarithm of $\mathbf{a}(s_k)$,

$$\bar{\mathbf{a}}(s_k) = \ln \mathbf{a}(s_k) \quad (17)$$

$$= \ln \left\{ \sum_{s_{k-1} \in A} \exp[\bar{\mathbf{a}}(s_{k-1}) + \bar{\mathbf{g}}(s_{k-1} \rightarrow s_k)] \right\} \quad (18)$$

$$= \max_{s_{k-1} \in A} * [\bar{\mathbf{a}}(s_{k-1}) + \bar{\mathbf{g}}(s_{k-1} \rightarrow s_k)] \quad (19)$$

where A is the set of states s_{k-1} that are connected to the state s_k .

Now let $\bar{\mathbf{b}}(s_k)$ be the natural logarithm of $\mathbf{b}(s_k)$,

$$\bar{\mathbf{b}}(s_k) = \ln \mathbf{b}(s_k) \quad (20)$$

$$= \ln \left\{ \sum_{s_{k+1} \in B} \exp[\bar{\mathbf{b}}(s_{k+1}) + \bar{\mathbf{g}}(s_k \rightarrow s_{k+1})] \right\} \quad (21)$$

$$= \max_{s_{k+1} \in B} * [\bar{\mathbf{b}}(s_{k+1}) + \bar{\mathbf{g}}(s_k \rightarrow s_{k+1})] \quad (22)$$

where B is the set of states s_{k+1} that are connected to state s_k , and we can calculate the Log Likelihood Ratio (LLR) by using

$$\Lambda_k = \ln \frac{\sum_{S_1} \exp[\bar{a}(s_k) + \bar{g}(s_k \rightarrow s_{k+1}) + \bar{b}(s_{k+1})]}{\sum_{S_0} \exp[\bar{a}(s_k) + \bar{g}(s_k \rightarrow s_{k+1}) + \bar{b}(s_{k+1})]} \quad (23)$$

where $S_1 = \{s_k \rightarrow s_{k+1} : m_k = 1\}$ is the set of all state transitions associated with a message bit of 1, and $S_0 = \{s_k \rightarrow s_{k+1} : m_k = 0\}$ is the set of all state transitions associated with a message bit of 0.

At the last iteration we make the hard decision by using the second decoder output $\Lambda^{(2)}$,

$$\hat{m}_k = \begin{cases} 1 & \text{if } \Lambda^{(2)} \geq 0 \\ 0 & \text{if } \Lambda^{(2)} < 0 \end{cases} \quad (24)$$

IV. PERFORMANCE OF TURBO CODED SIGNALS

In this section, the performance of turbo coded signals are evaluated for different frame numbers and memory constellations over Rician fading channels.(Figures 5-14).

In our example, 1/2 and 1/3 rate turbo encoders with different memory configurations are investigated. Here we use the generator matrix $g=[111:101]$, $g=[1101:1111]$ and $g=[11111:10001]$, a random interleaver is used and the frame size $N=500$. It is clear that for a constant iteration number, as K increases, performance improves for the same SNR values. And also the frame number is the important effect for improving the performance as shown in Figure 7 and 8.

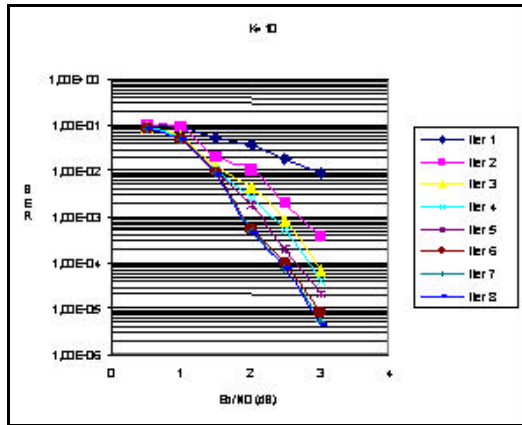


Figure 5. Bit error performance for K=10, $g=[111:101]$ and $N=500$

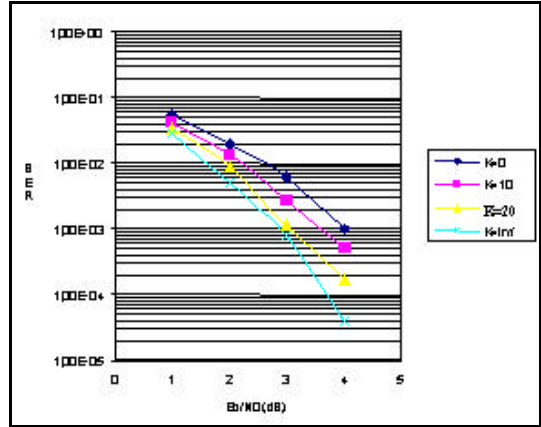


Figure 6. Bit error performance for K=10, $g=[111:101]$ and $N=192$

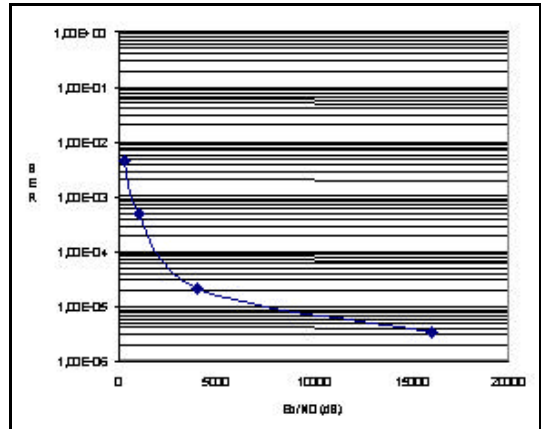


Figure 7. Bit error performance for $r=1/3$ $E_b/N_0=4\text{dB}$ $g=[111:101]$ and $N=500$ over $K=0$ dB

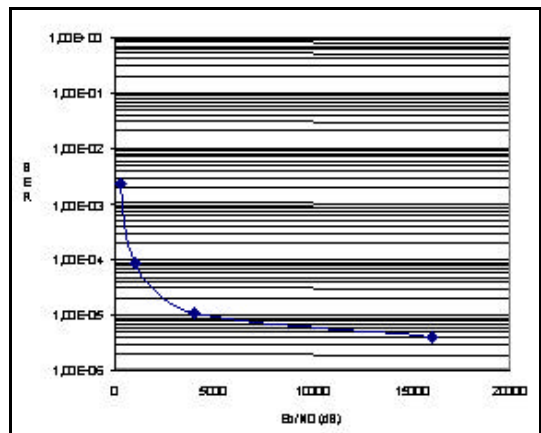


Figure 8. Bit error performance for $r=1/3$ $E_b/N_0=1.5\text{dB}$ $g=[111:101]$ and $N=500$ over AWGN channel

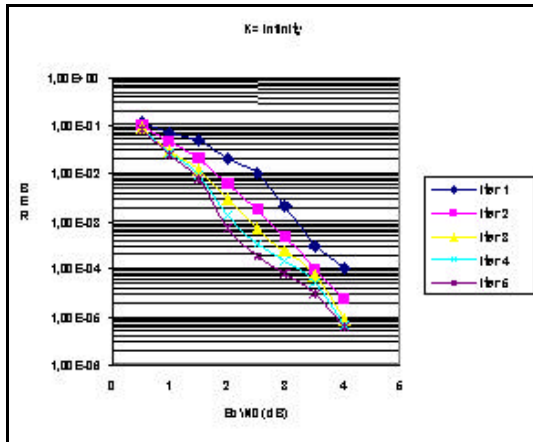


Figure 9. Bit error performance for $r=1/2$
 $g=[1101;1111]$ and $N=500$ over AWGN channel

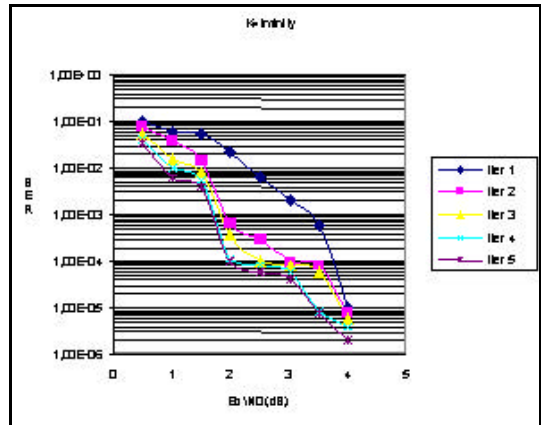


Figure 12. Bit error performance for $r=1/2$
 $g=[11111;10001]$ and $N=500$ over AWGN channel

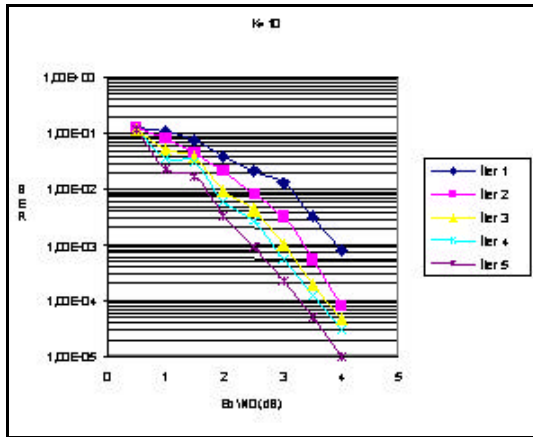


Figure 10. Bit error performance for $r=1/2$,
 $g=[1101;1111]$, $N=500$ and $K=10\text{dB}$

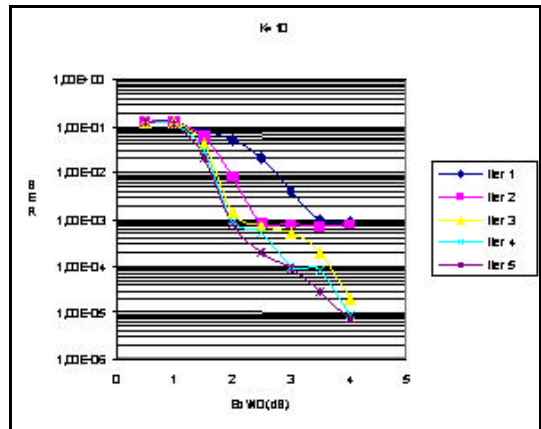


Figure 13. Bit error performance for $r=1/2$
 $g=[11111;10001]$, $N=500$ and $K=10\text{dB}$

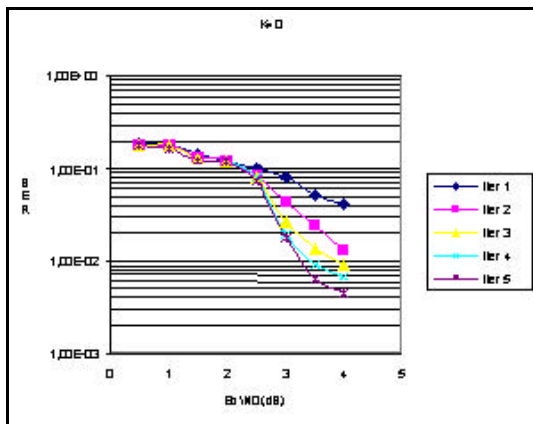


Figure 11. Bit error performance for $r=1/2$,
 $g=[1101;1111]$, $N=500$ and $K=0\text{dB}$

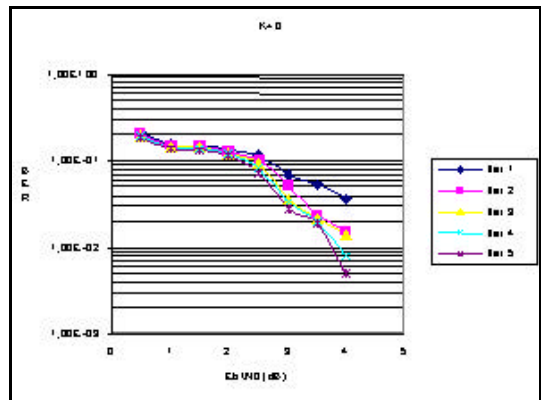


Figure 14. Bit error performance for $r=1/2$
 $g=[11111;10001]$, $N=500$ and $K=0\text{dB}$

V. CONCLUSION

In this paper we show how encoder structures encoder rate and frame number can be altered the performance while Rician channel is under consideration. As an example, the performance of turbo coded signals are simulated over Rician channel with different fading parameter, iteration number and data block size N .

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